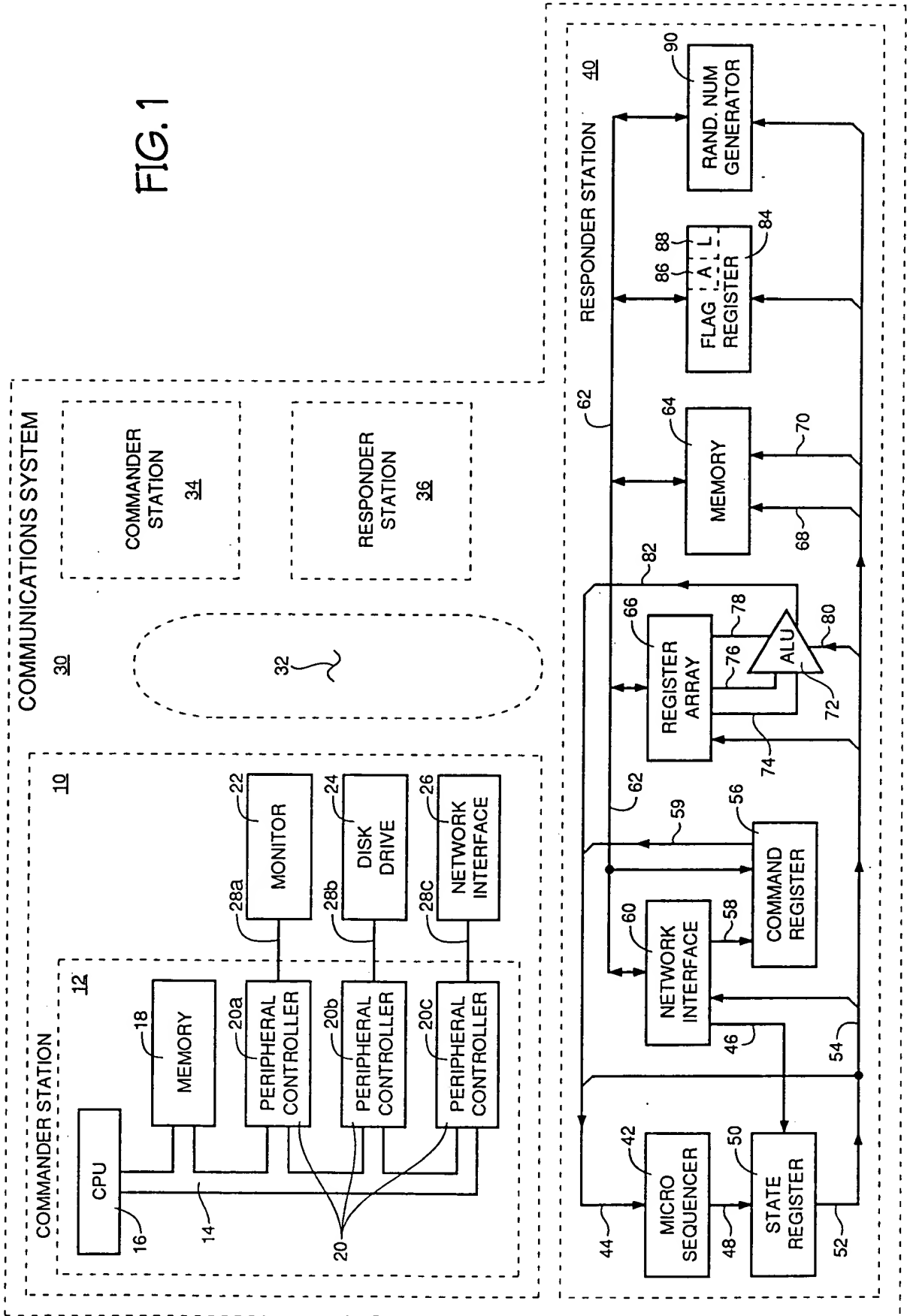


FIG. 1





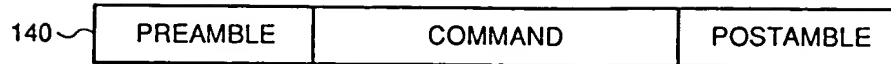


FIG. 4

| COMMANDS | | | |
|-------------------------|----------|---|--------|
| OPCODE (HEXADECIMAL) | MNEMONIC | DESCRIPTION | FORMAT |
| 08 | ID | IDENTIFY | 142 |
| A9 | IDG | IDENTIFY AND GENERATE ARBITRATION NUMBER | 142 |
| EA | IDC | IDENTIFY AND CLEAR LOCKED BIT 88 | 142 |
| 4B | IDCG | IDENTIFY, CLEAR LOCKED BIT 88, AND GENERATE ARBITRATION NUMBER | 142 |
| 8E | RD | READ DATA AND SET LOCKED BIT 88 | 144 |
| 2F | WD | WRITE DATA AND SET LOCKED BIT 88 | 146 |

FIG. 5

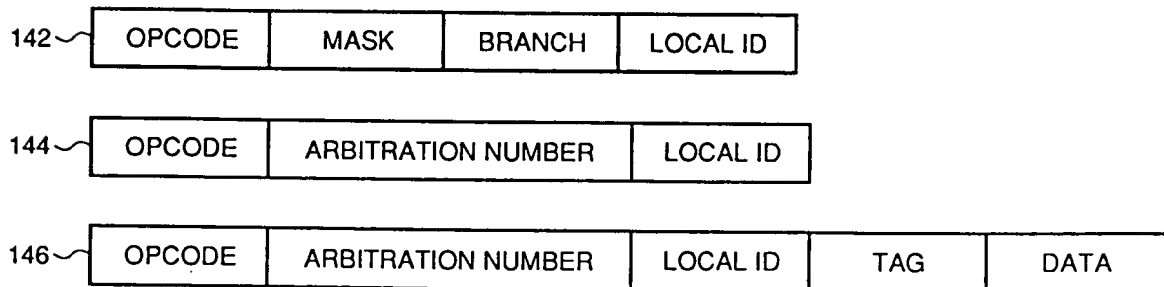


FIG. 6

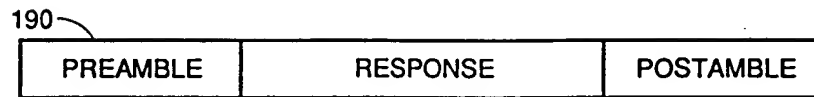


FIG. 7

| RESPONSES | | |
|-----------|---|--------|
| MNEMONIC | DESCRIPTION | FORMAT |
| IDR | RESPONSE TO ID, IDG, IDC, AND IDCG COMMANDS | 192 |
| RDR | RESPONSE TO RD COMMAND | 194 |
| WDR | RESPONSE TO WR COMMAND | 196 |

FIG. 8

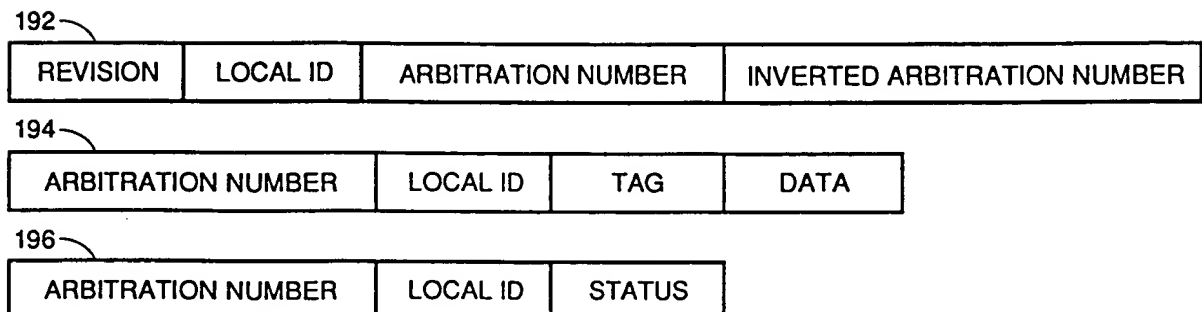


FIG. 9

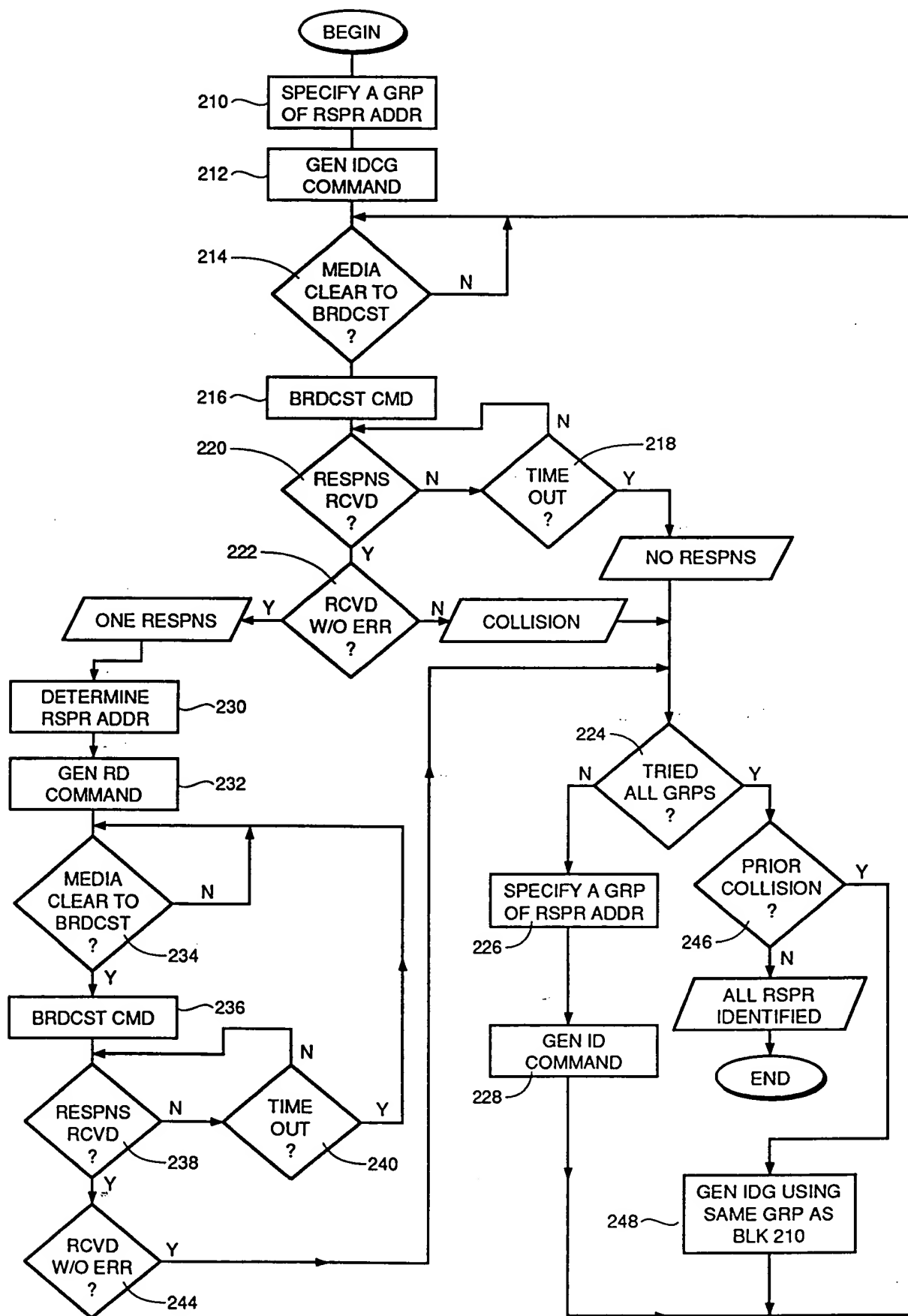
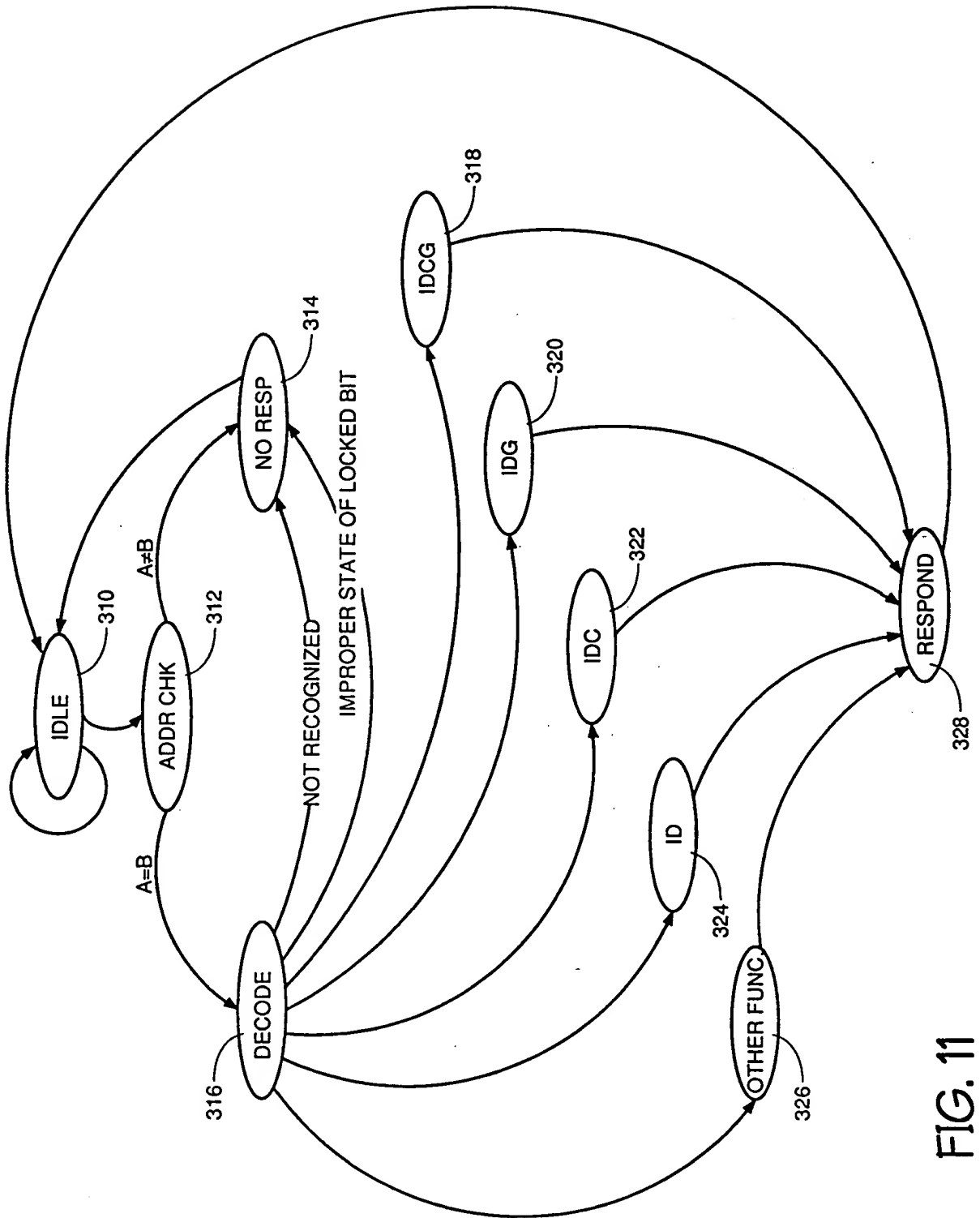


FIG. 10



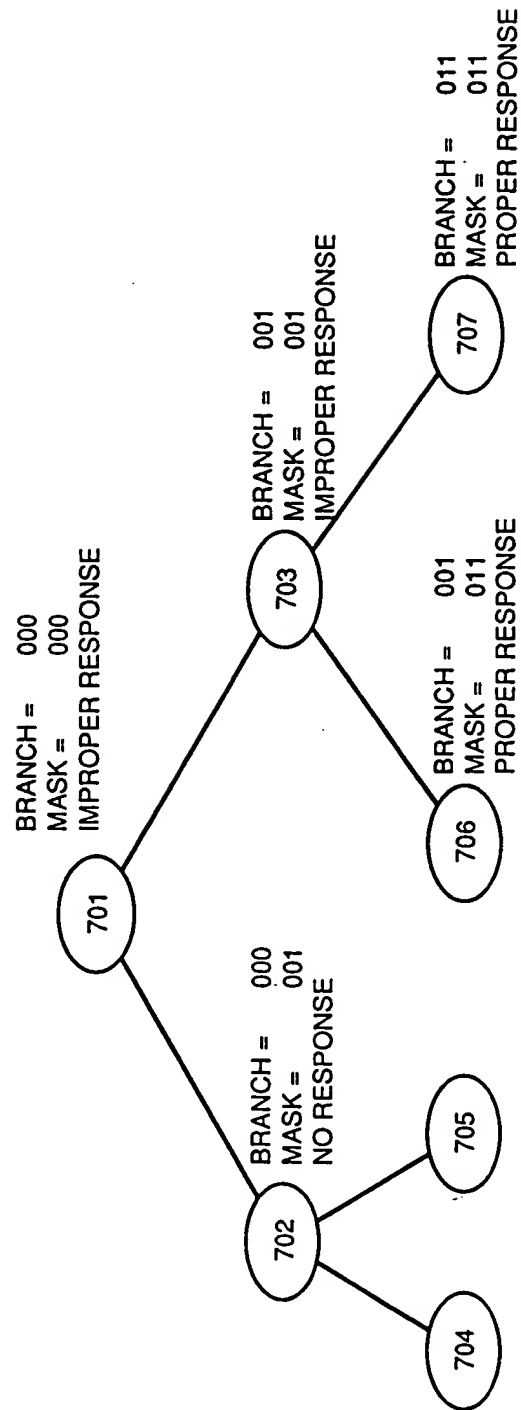


FIG. 12

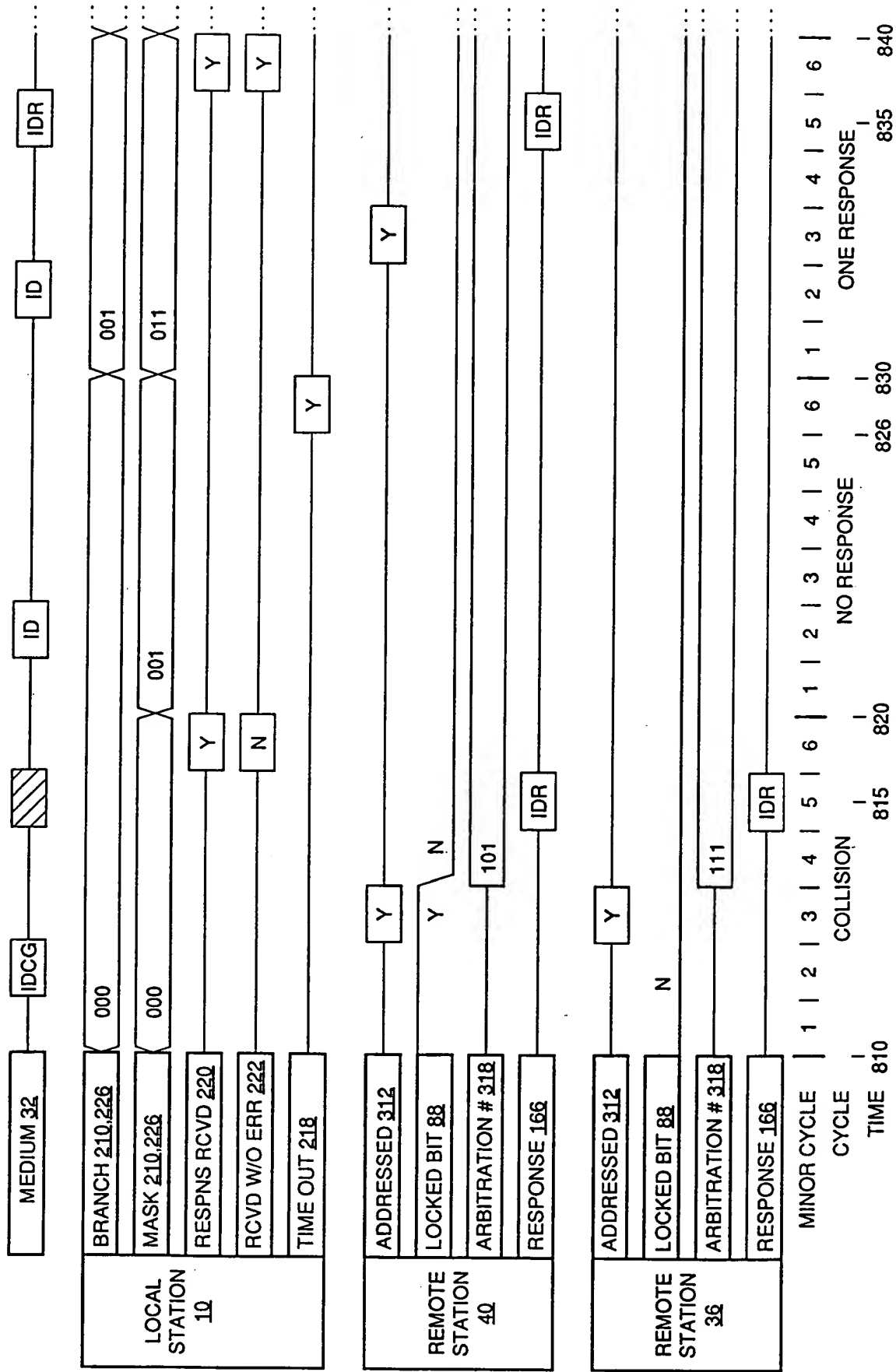


FIG. 13

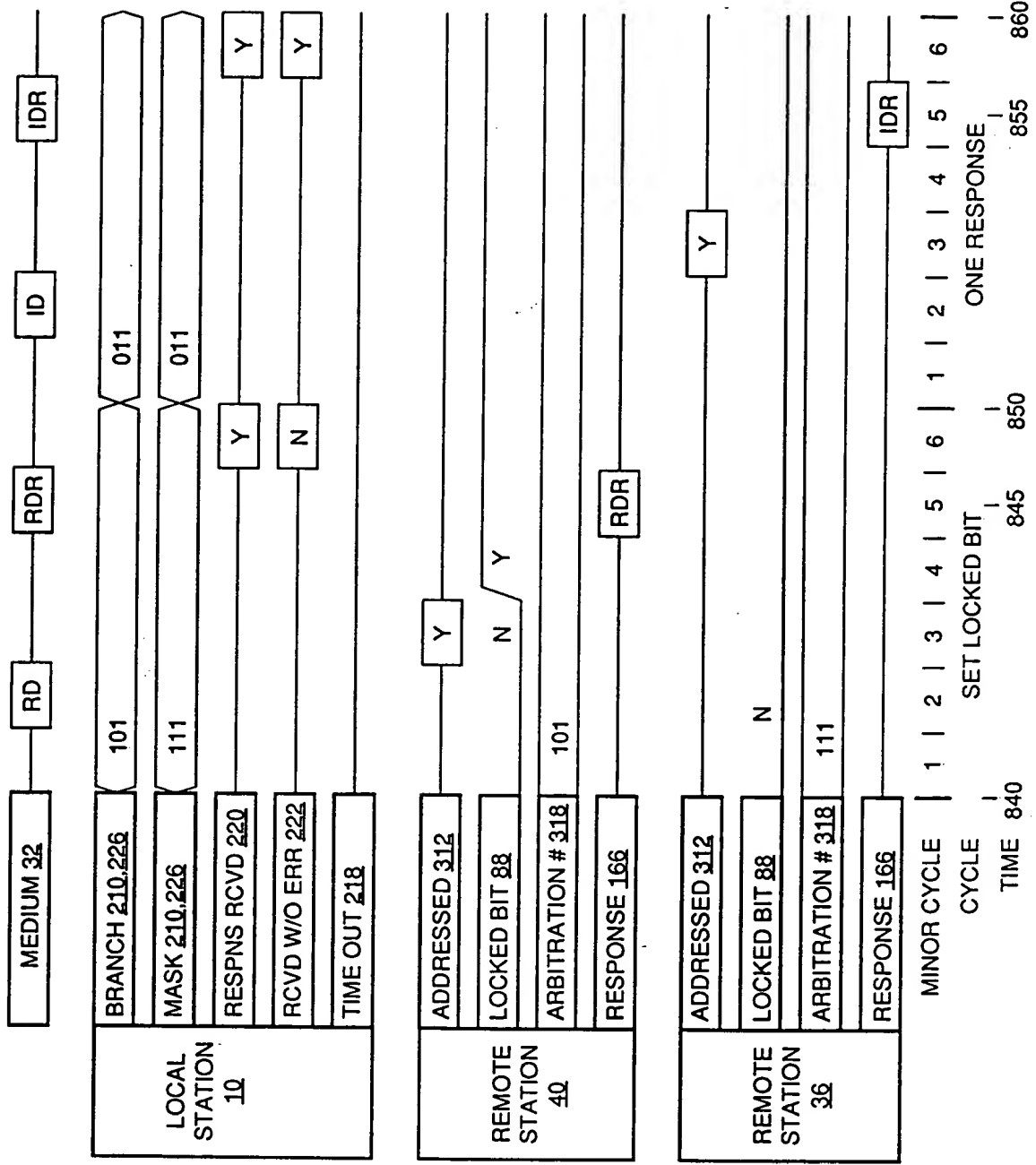


FIG. 14

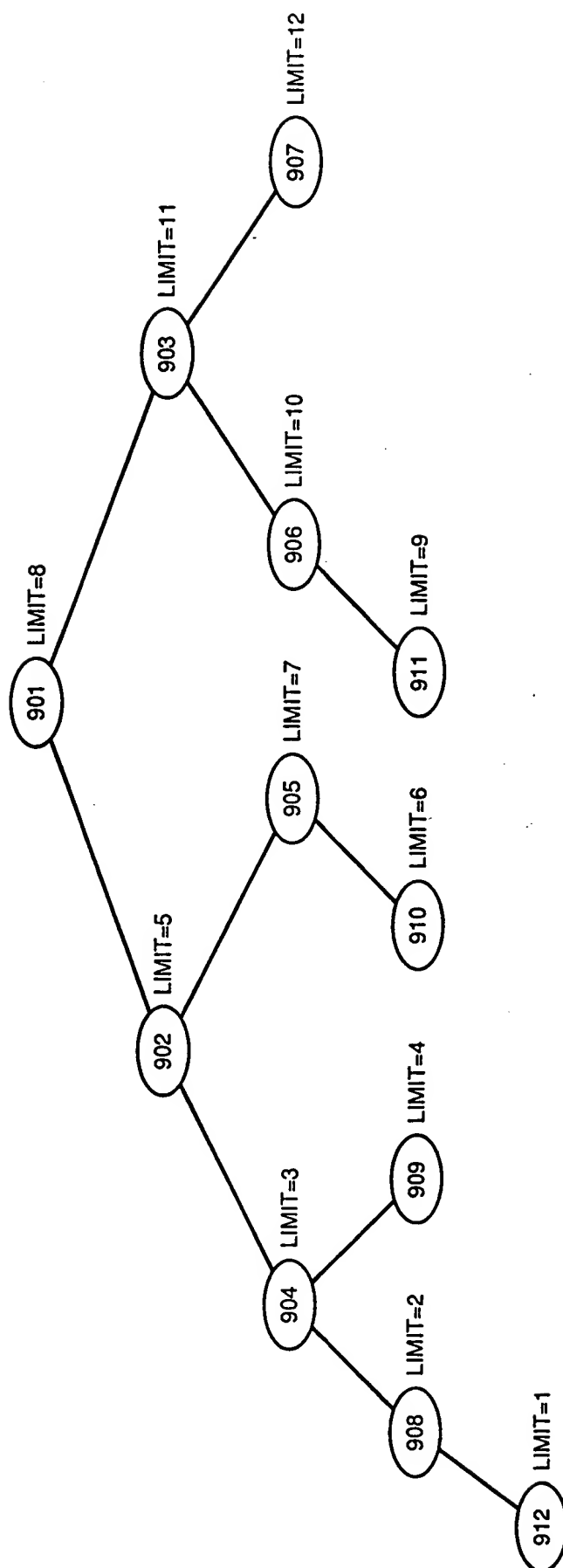


FIG. 15